

8.5/11/02

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Akram et al.

Serial No.: 09/652,585

Filed: August 31, 2000

For: ELECTRICAL CONNECTOR

Examiner: R. Chang

Group Art Unit: 3729

Attorney Docket No.: 5095.5US (97-1102.5)

CERTIFICATE OF MAILING

I hereby certify that this correspondence along with any attachments referred to or identified as being attached or enclosed is being deposited with the United States Postal Service as First Class Mail (under 37 C.F.R. § 1.8(a)) on the date of deposit shown below with sufficient postage and in an envelope addressed to the Commissioner for Patents, Washington, D.C. 20231.

July 22, 2002 Date of Deposit

Signature of registered practitioner or other person having reasonable basis to expect mailing to occur on date of deposit shown pursuant to 37 C.F.R. § 1.8(a)(1)(ii)

Deidra Pfeil
Typed/printed name of person whose signature is contained above

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents Washington, D.C. 20231

Sir:

In compliance with the duty to disclose information material to patentability pursuant to 37 C.F.R. § 1.56, it is respectfully requested that this Supplemental Information Disclosure Statement be entered and the document listed on attached Form PTO-1449 or PTO/SB/08A be considered by the Examiner and made of record. The listed document is from co-pending application Serial No. 09/652,923, filed August 31, 2000. A copy of the listed document is enclosed pursuant to 37 C.F.R. § 1.98(a).

In accordance with 37 C.F.R. § 1.97(g) and (h), filing of this Supplemental Information Disclosure Statement is not to be construed as a representation that a search has been made or an admission that the information cited herein is, or is considered to be, material to patentability as

defined in 37 C.F.R. § 1.56(b). Further, no representation is made by Applicants herein that no other possible material information as defined in 37 C.F.R. § 1.56(b) exists.

DOCUMENTS

U.S. Patent Documents

U.S. Patent No. <u>Issue Date</u> <u>Inventor</u> 5,737,818 04/1998 Frank et al.

Applicants offer to supply any explanation or discussion of the document which the Examiner feels is necessary or desirable and which is requested.

This Supplemental Information Disclosure Statement is filed after the mailing date of the first Office Action on the merits. I hereby certify that no item of information contained in the Supplemental Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned after making reasonable inquiry, was known to any individual designated in 37 C.F.R. § 1.56(c) more than three months prior to the filing of the statement.

Respectfully submitted,

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Date: July 22, 2002

JRD/sls:djp

Enclosures: Form PTO-1449 or PTO/SB/08A

Copy of document cited

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Salman Akram and Warren M. Farnworth

Serial No.: 09/652,585

Filed:

August 31, 2000 ELECTRICAL CONNECTOR

Examiner:

§ §

§ § Group Art Unit:

Atty. Docket: 97-1102.05

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents Washington, D.C. 20231

Dear Sir:

Certificate of Mailing (37 C.F.R.§ 1.8) I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, postage prepaid, in an envelope addressed to Assistant Commissioner for Palents, Washington, D.C. 20231, on the date below 11/4/00

In compliance with the duty of disclosure under 37 C.F.R. § 1.56, Applicants respectfully request that this Information Disclosure Statement be entered and that the references listed on the attached Form PTO-1449 be considered by the Examiner and made of record. Copies of the listed references are enclosed for the convenience of the Examiner.

In accordance with 37 C.F.R. § 1.97(b), this Information Disclosure Statement is not to be construed as a representation that a search has been made or that no other possible material information as defined in 37 C.F.R. § 1.56(a) exists.

The following references are submitted for the Examiner's review:

U.S. Patents

U.S. Patent No. Issue Date Inventor 5,962,921 October 5, 1999 Farnworth et al. 5,545,050 August 13, 1996 Sato, et al.

Other References

- "Condel Test Socket Specifications," Liberty Research Socket Specifications, Web Page: www.liberty-research.com/sktspcs.htm; February 10, 1998.
- "Holeshot Test Socket and Adapter for Area Array Packages," Liberty Research, Web Page: www.liberty-research.com/holeshot.htm; February 10, 1998.
- "Penta O Probe Sheet," Interconnect Devices, Inc., Catalog page.
- "Plunger Tip Styles by Size," Catalog page from Interconnect Devices, Inc.
- "Probe Plunger Tip Application Guide," Interconnect Devices, Inc., Web Page: www.idinet.com/aguide.htm; December 20, 1998.
- "Series 654 Ball Grid Array Socket (on 1.27 mm pitch); catalog page, Wells Electronics, Inc., South Bend, Indiana.
- "Series 654 Ball Grid Array Socket (on 1.5 mm pitch); catalog page, Wells Electronics, Inc., South Bend, Indiana.
- "Series 655 OTBGA Burn-In/Test Socket (Pinch Contact)," Wells-CTI, Web Page: www.wellscti.com/ser655p.htm; December 16, 1998.
- "Series 655 OTBGA Socket (Y Contact)," Wells-CTI, Web Page: www.wellscti.com/ser655.htm;

 December 16, 1998.
- "Test Sockets and Adapters for Area Array Packages," Liberty Research, Web Page: www.liberty-research.com/arayinfo.htm; February 10, 1998.

As this information is being submitted within three months of the date of filing of the application,

Applicants understand that no fee or certification is required for the submission and consideration of this information at this time.

If there are any matters which may be resolved or clarified through telephone interview, the Examiner is respectfully requested to contact Applicants' undersigned attorney at the number indicated.

* * * *

A Form PTO-1449 is enclosed herewith.

Respectfully submitted,

Date: 11/5/00

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